

ST33 Series

Bill Validators

Operation Manual

Contents

	Page
1. Overview	3
2. Main Parts	4
3. Precautions	5
4. Banknote Stacking	5
5. Banknote Jam	6
6. Maintenance	6
7. Mounting	7
9. Input And Output Circuitry	8
10. Harness Connection	10
11. Connector Pinout	11
12. DIP Switch Settings.....	12

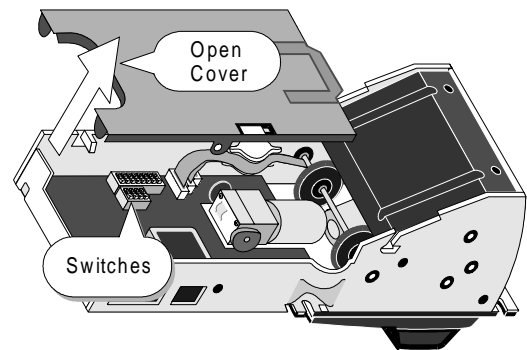
1. Overview

⚙ The Bill Validator can accept a banknote lengthways in all four directions i.e. face up, face down either end first regardless of its positioning in the entry slot.

⚙ It takes only 2 seconds to transport, identify and stack each banknote from start to finish.

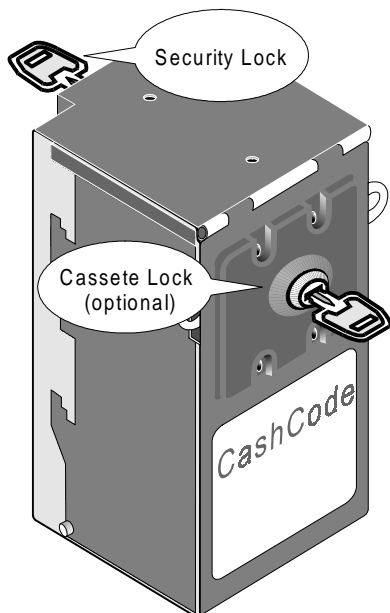
⚙ The acceptance rate is 95% or greater for street banknotes.

⚙ The Bill Validator is equipped with DIP switches allowing the user to select the operating mode.



Banknote depositing.

⚙ The Bill Validator has an ESCROW feature, which after validating a banknote and sending a message containing information about the banknote denomination to the external equipment, holds the banknote in a position from which it can either be returned or accepted by the next command sent by external equipment.



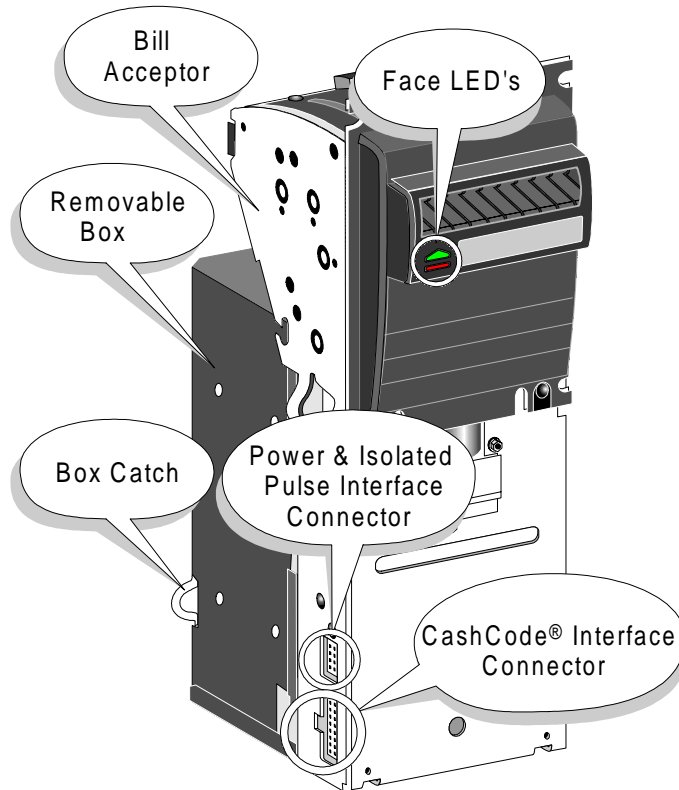
⚙ The Bill Validator stacks accepted banknotes into a high security lockable and removable cassette (LRC). The design of the Cassete guarantees reliable protection against unauthorised access to its contents.

2. Main Parts

The Bill Validator consists of the Bill Validator itself and the Cassette. There are Cassettes of different capacities: for 170, 400, 600 and 1000 banknotes.

There are two LED indicators on the front panel of the Bill Validator. The first indicator is green and triangular, the second indicator is red and rectangular. When the Bill Validator is in stand-by mode and ready to accept a banknote, the green triangular indicator is ON. The red rectangular indicator is ON if the Bill Validator can not accept a banknote for some reason (the previous banknote is still being processed, or the acceptance is prohibited by the external equipment, or the Cassette is full, or in case of any malfunction).

There are connectors on the side of the Bill Validator to connect it to the external equipment.



3. Precautions

1. Badly ripped, wrinkled, or wet banknotes may get jammed in the Bill Validator.
2. The electronic components should not be allowed to get wet at any time.
3. A lot of dust entering the inside of the Bill Validator may interfere with the optical sensors as well as the transport mechanism.

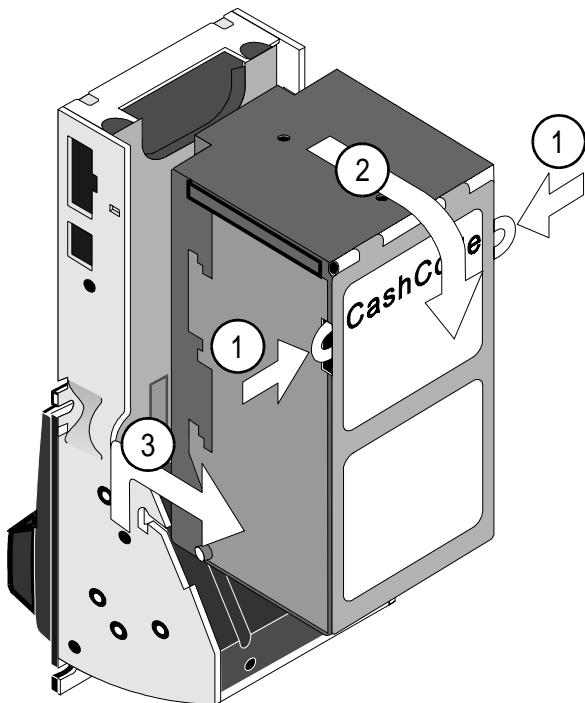
4. Banknote Stacking

Accepted banknotes are stacked in the removable Cassettes. Depending on the position of DIP switch SW1-5, the Bill Validator can accept banknotes either one way or four ways. Accordingly, the banknotes in the Cassette take either identical positions or all possible random positions.

Various models of the Cassettes allow for different capacities and also different security features.

To take the banknotes out of the Cassette, perform the following:

1. Remove the Cassette from the Bill Validator by pressing with your fingers against both Cassette catches, while rotating the cassette at an angle of 15...20 degrees around the axis via which the cassette is hooked to the Bill Validator.



2. Move the Cassette along the hooking slots in the base of the Bill Validator until the axis is free.

3. Take the Cassette off the Bill Validator by moving it out along the slots in the Bill Validator.

5. Banknote Jam

To remove a jammed banknote from the Bill Validator, perform the following:

1. Remove the Cassette carefully, trying not to damage the jammed banknote.
2. Open the two covers of the banknote pathway.
3. Remove the jammed banknote.

6. Maintenance

Dirt in the pathway, optical sensors, or magnetic head may affect the acceptance rate of the Bill Validator or even cause jams. To clean the pathway, do as prescribed below:

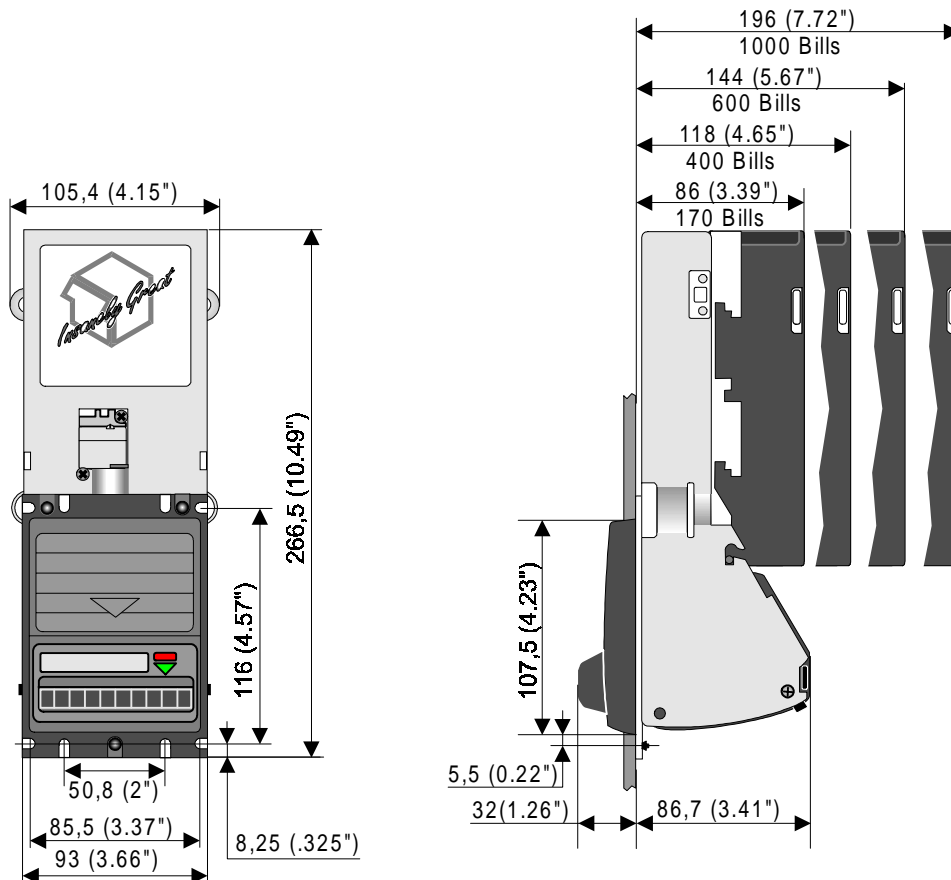
1. Remove the cassette from the Bill Validator
2. Open the two covers of the banknote pathway "clam shell".
3. Use a soft cloth moisten with mild, non-abrasive detergent to remove the dirt.

Note: Bill Validators should be cleaned as needed to maintain optimal performance. This will vary depending on location and number of bills inserted.

DO NOT USE STRONG THINNERS LIKE ACETONE OR GASOLINE AS IT MAY CAUSE DAMAGE TO PLASTIC PARTS OF THE BILL VALIDATOR.

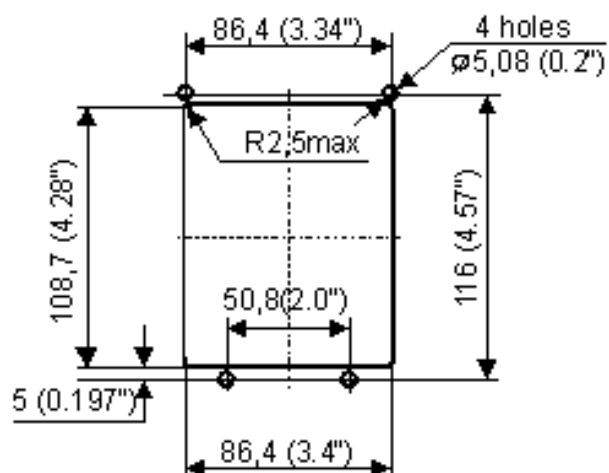
7. Mounting

The overall and mounting dimensions are given below. They differ for Cassettes of different



capacities.

Layout of Mounting Holes



The figure «Layout Of Mounting Holes» shows the template for panel of the external equipment where the Bill Validator is to be mounted. All the dimensions are in millimetres and inches (in parenthesis).

8. Input and Output Circuitry

The Bill Validator has two types of input / output circuitry according to the existing interfaces: isolated low voltage circuitry and TTL/CMOS-level non-isolated circuitry.

8.1. Isolated Pulse Interface

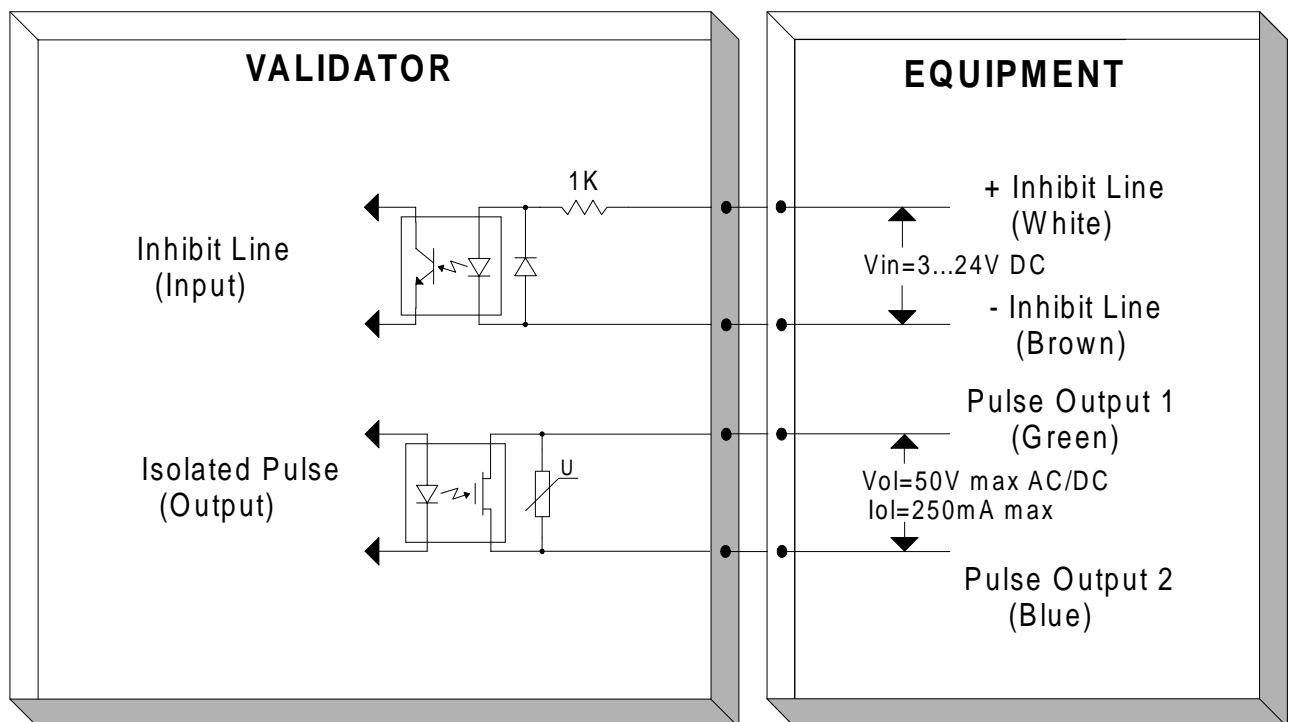
The input and output circuitry belonging to this interface are shown in the figure below. They have the following specifications:

1. Isolated OUTPUT PULSE circuitry:

- normally open (NO) relay bounce free contact with optocouple electrical insulation;
- open circuit leakage current is not greater than 0.1 mA with applied voltage up to 50 VDC;
- resistance of the closed contacts is not greater than 100 Ohm;
- maximum load current is not more than 70 mA.

2. INHIBIT LINE input:

- electrically insulated optocouple input;
- input voltage to prohibit any banknote acceptance is not more than +0.5 VDC;
- input voltage to allow banknote acceptance is within the range: +3...+24 VDC with the current not exceeding 24 mA.



The additional non-isolated TTL-level CREDIT PULSE output has exactly the same circuitry as output circuitry of the CashCode® Serial Interface (see unit 8.2)

8.2. CashCode® Serial Interface

The input and output circuitry belonging to this interface are shown on the figure below. They have the following specifications:

1. The output:

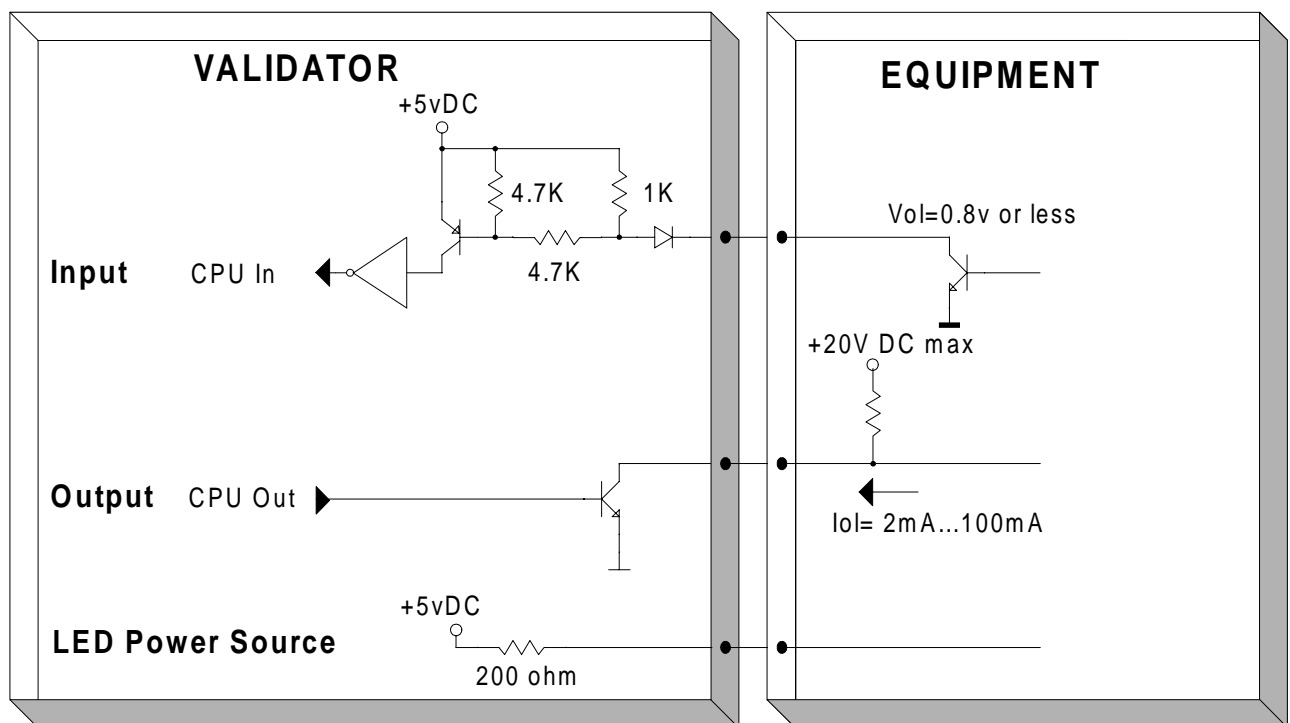
- open collector type
- open transistor leakage current is not greater than 0.1 mA with applied voltage up to 20 VDC;
- closed transistor collector-emitter voltage is not more than +0.6 VDC with current up to 100 mA.

2. The input:

- logic «0» state input voltage is not more than +0.5 VDC with sinking current up to 5 mA;
- logic «1» state input voltage is not less than +4.5 VDC with sourcing current up to 0.1 mA.

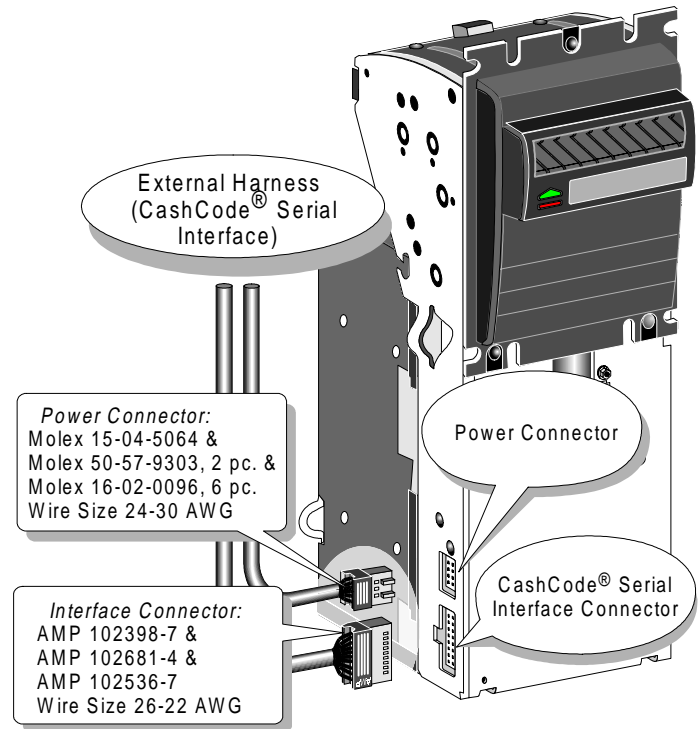
The output circuitry is compatible with any of the input circuitry of the external equipment based on TTL, HC or HCT gates levels. To guarantee a reliable logic «1» state output signal, the external equipment should have a pull-up resistor with resistance within 1 kOhm... 4.7 kOhm.

The input circuitry can be connected directly to the output of components such as TTL, HC, HCT series of the external equipment. On top of this, any component of the external equipment can provide the input signal within the specs for the input circuitry.

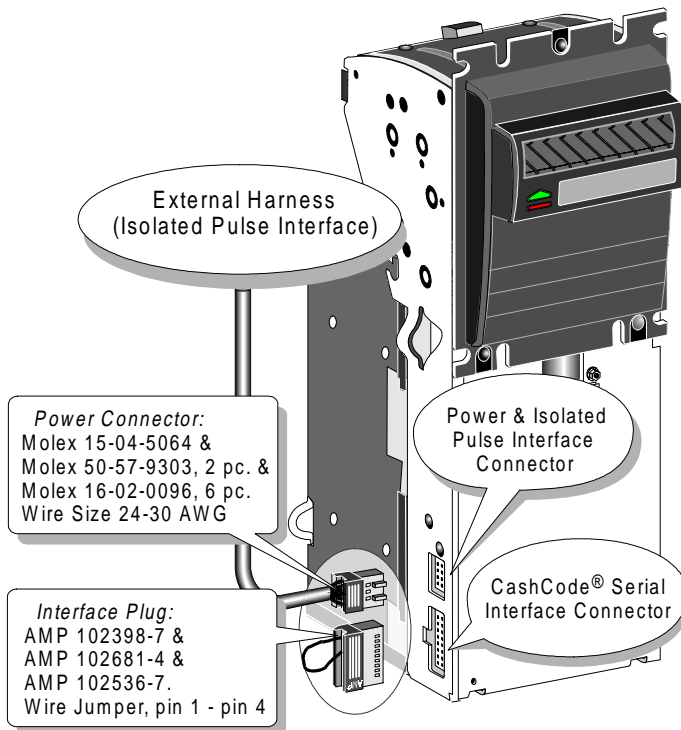


9. Harness Connection

1. When used in the external equipment supporting the CashCode® Serial Interface or Mars Serial Interface which is compatible with it, the Bill Validator must be connected with both the 6 Pin Power Harness PT# OPT-HS-12v-06p and the 18 Pin Serial Harness PT# OPT-HS-CS0-18p.

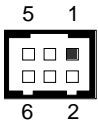
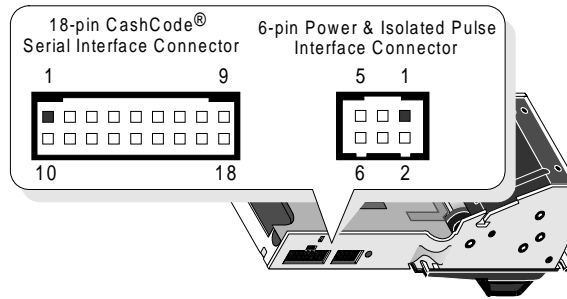


2. When used in the external equipment supporting Isolated Pulse Interface, the Bill Validator is to be connected with the 6 Pin Power Harness PT# OPT-HS-12v-06p; and the Jumper Connector PT# OPT-HS-JC18 may be applied to the 18 pin interface connector of the Bill Validator if required.



10. Connector Pinout

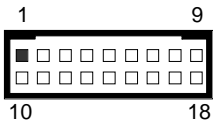
Power & Isolated Pulse Interface Connector



Mating Connector: Molex, Part #:15-04-5064, 1 psc;
50-57-9303, 2 psc;
16-02-0096, 6 psc.

Terminal	Signal	Function	Wire Colour
1	Power Supply (+)	+ 12 V DC Power	Yellow
2	Power Supply (-)	Ground	Black
3	Output Pulse 1	Pulse Signal	Blue
4	Output Pulse 2	Pulse Signal	Green
5	+ Inhibit Line	Enable/Disable	White
6	- Inhibit Line	Accept Bill	Brown

CashCode® Serial Interface Connector



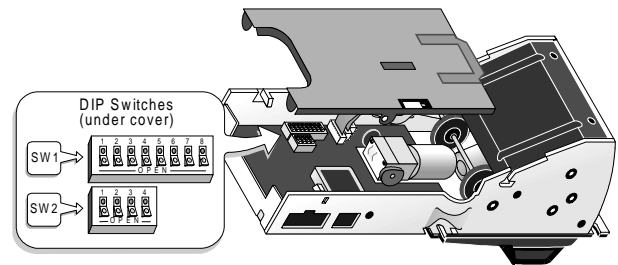
Mating Connector: AMP, Part #:102398-7, 1 psc;
102536-7, 1 psc;
102681-4, 1 psc.

























Terminal	Signal	Function	Activity
1	Credit Pulse	Output	Low
2	Interrupt	Output	Low
3	Serial/Pulse Select		High/Low
4	Ground	Signal Ground	
5	Serial Data Output		
6	Not connected	-----	
7	Not connected	-----	
8	Not connected	-----	
9	Not connected	-----	
10	Out of Service	Output	Low
11	Not connected	-----	
12	Accept Enable	Input	Low
13	LED Power Source	Output	Low
14	Send	Input	Low
15	Not connected	-----	
16	Not connected	-----	
17	Not connected	-----	
18	Not connected	-----	

11. DIP Switch Settings

The Bill Validator is equipped with an eight position DIP switch and four position DIP switch (optional). These switches allow the user to select an operating mode of the Bill Validator. They are located at the back side of the Bill Validator under the cover protecting the CPU board. They can be reached through special openings in the cover only if the Cassette is removed.

To change the position of a DIP switch, use the point of a sharp instrument.



SW 1 Switch Description	
 - 1 pulse / \$1	 - 2 pulse / \$1
 - 3 pulse / \$1	 - 4 pulse / \$1
 - \$1 Enable	 - \$1 Disable
 - \$5 Enable	 - \$5 Disable
 - One Way	 - Four Way
 - Stacker Up	 - Stacker Down
 - Slow Pulse 50mS ON/300mS OFF	 - Fast Pulse 50mS ON/50 mS OFF
 - Isolated Pulse Interface	 - All Interfaces
SW 2 Switch Description	
 - \$50 Enable	 - \$50 Disable
 - \$100 Enable	 - \$100 Disable
 - High Security	 - High Accept
 - Reserved	 - Reserved

Each switch is assigned to control the settings shown below.

* This is an example only - please check your USER MANUAL for your model.